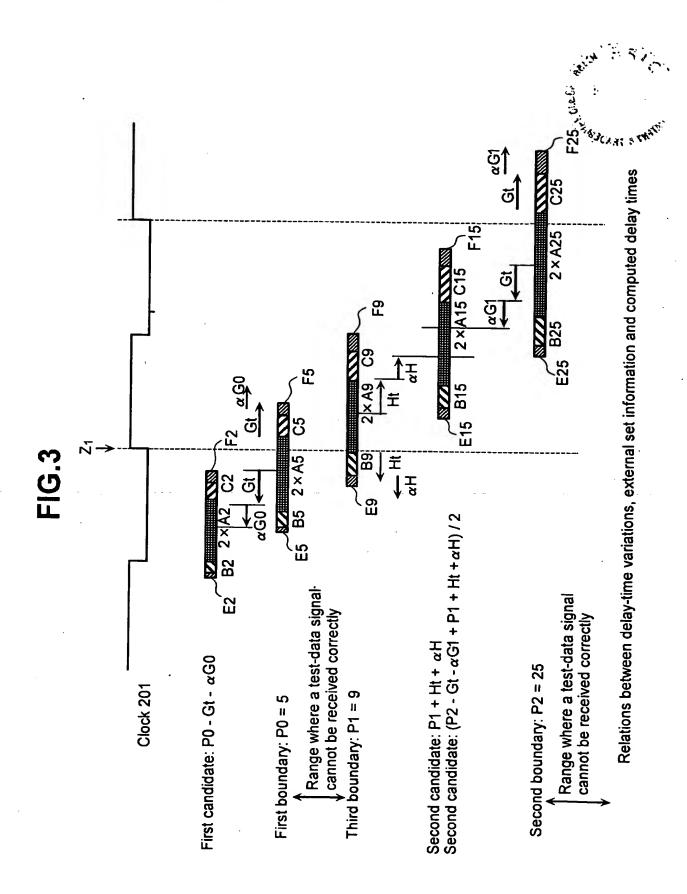


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Phase relations between received-data indeterminate times and a clock signal

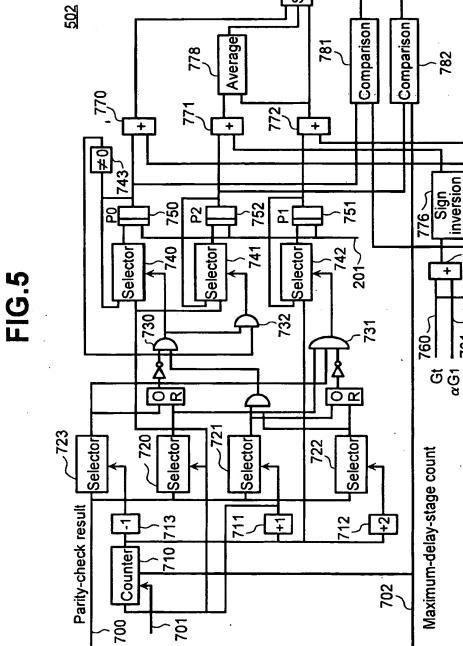


16869P-027200US Chun-Pok Leung, Reg. No. 41,405 Telephone: 650-326-2400 Inventors: Yuji Saeki Sheets of drawings 4 of 9

790

Selector

780



Typical circuit configuration of delay-time-computing unit

764

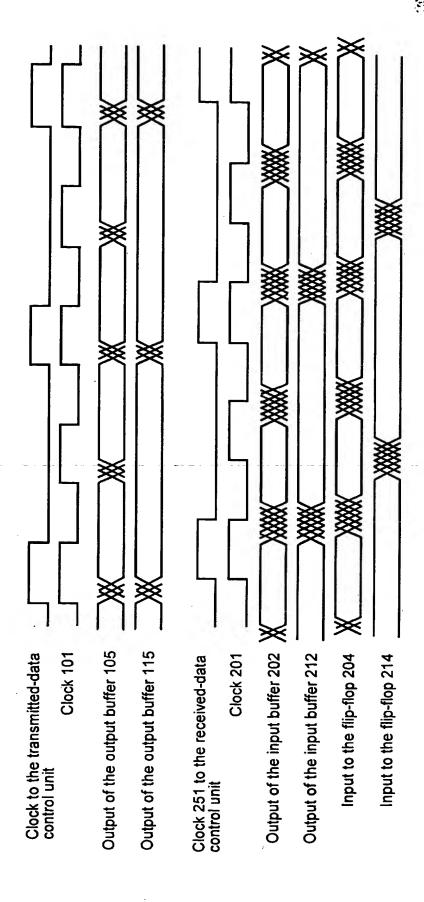
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Sign inversion

+

762 763

gg #



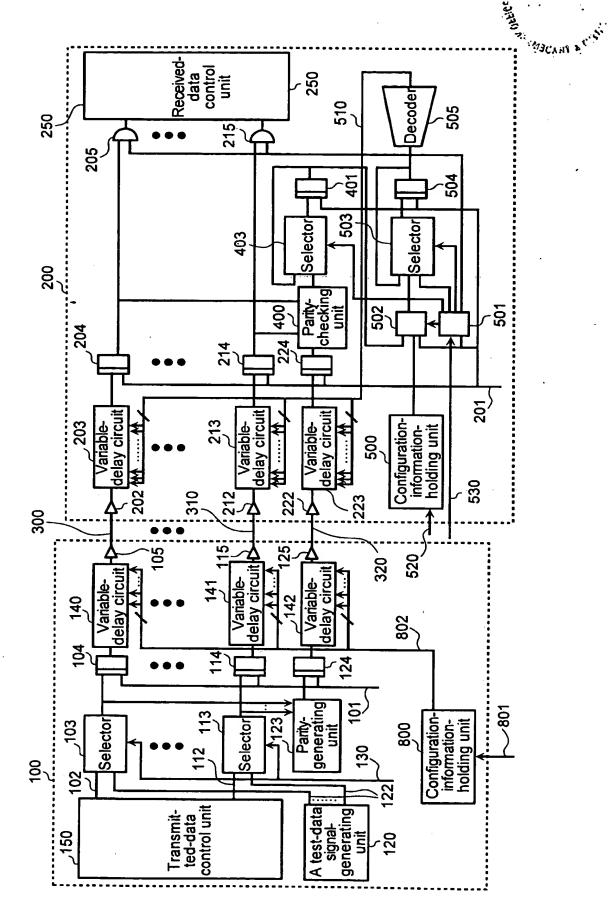
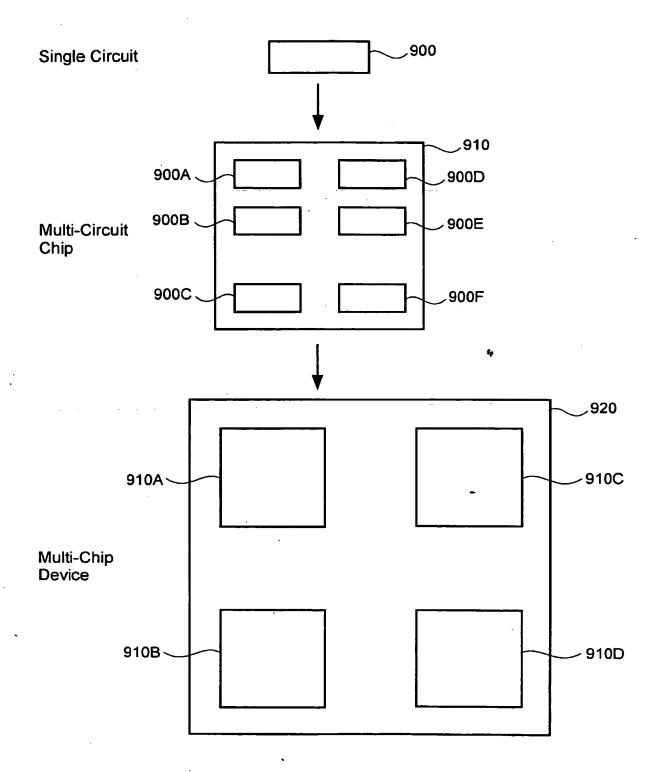


FIG.8

FIG.9



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